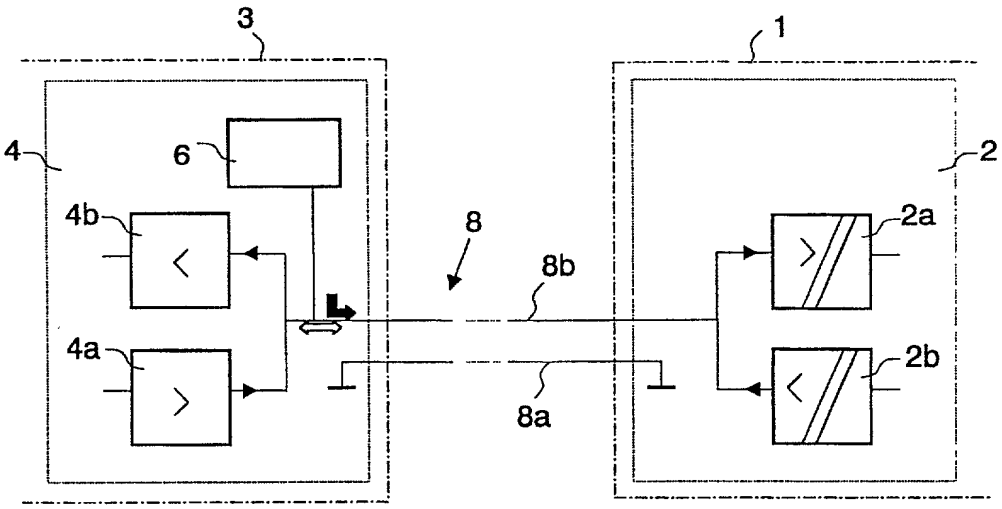
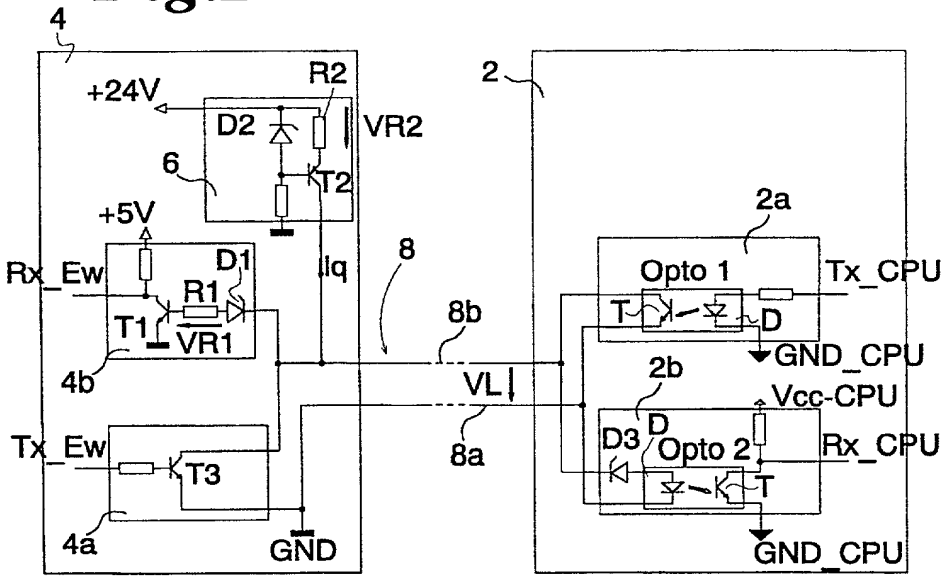

3
1
4
6
2
4b
8
2a
8b
4a
2b
8a

Fig.1

Fig.2


4
2
+24V
R2
D2
VR2
6
T2
+5V
2a
Rx_Ew
D1
Iq
8
Opto 1
Tx_CPU
R1
T
D
8b
T1
VR1
GND_CPU
4b
2b
VL
Vcc-CPU
Tx_Ew
T3
D3
D
Opto 2
Rx_CPU
8a
T
4a
GND
GND_CPU

Replaced by 10/009051

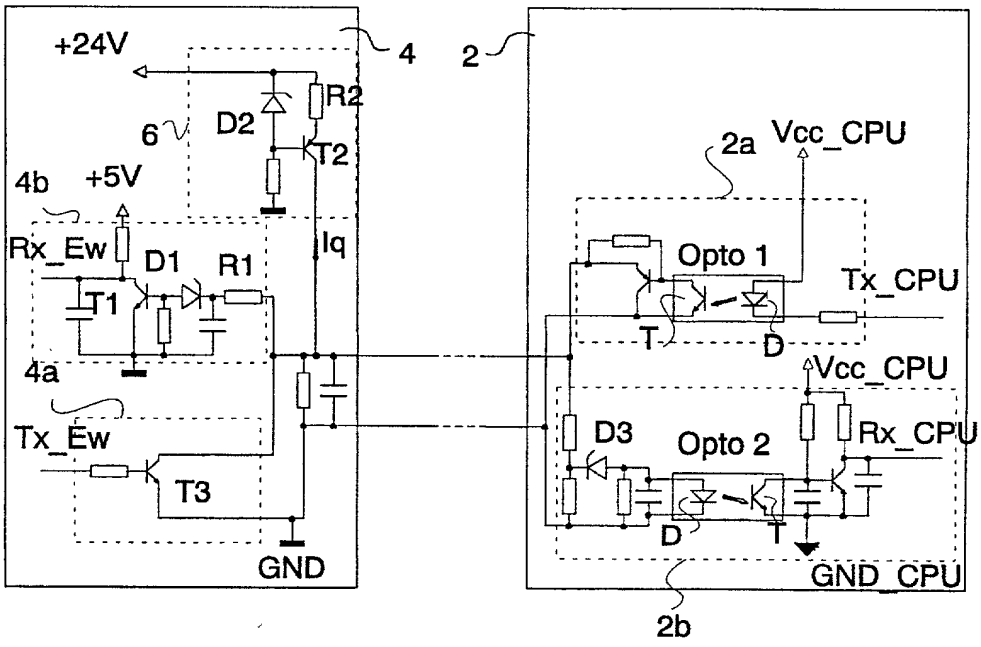



Fig.3